

In the Claims

Please amend the claims as follows:

1 1. (Amended) A method for emulation communications via a test
2 data input port and boundary-scan architecture providing serial
3 access to a serial connection of a plurality of registers disposed
4 in a plurality of modules, each of the plurality of modules
5 including at least one of the plurality of registers, comprising
6 the steps of:
7 selecting for communication one of said plurality of modules,
8 nonselected modules being nonresponsive to data on said serial
9 connection;
10 supplying to the test data input port for communication to the
11 boundary-scan architecture a serial signal having a first logic
12 state for a number of cycles greater in number than a number of
13 bits of the serial connection of the plurality of registers;
14 following supply of said serial signal, supplying to the test
15 data input port for communication to the boundary-scan architecture
16 a start bit having a second logic state opposite to said first
17 logic state followed by a predetermined number of data bits;
18 at said selected module detecting said start bit within the
19 boundary-scan architecture and storing said predetermined number of
20 data bits.

1 4. (Amended) The method of claim 1, wherein the boundary-scan
2 architecture includes a test data output port following a last of
3 the serial connection of registers, the method further comprising:
4 at said selected module, supplying a serial signal having said
5 first logic state to following registers in the serial connection
6 of the plurality of registers for a predetermined number of cycles
7 and supplying to following registers in the serial connection of
8 the plurality of registers a start bit having a second logic state